



Introduction

“Embedded System” refers to electronic systems which include some data processing capability. As opposed to general purpose computers, they are usually designed for a specific purpose. Some examples are watches, MP3 players, cell phones, routers or automotive control units.

The “brain” in an embedded systems is typically a microcontroller, although data can also be processed by a DSP, an FPGA, or an ASIC. The system design is usually optimized to reduce size, cost or power, and improve its reliability and performance.

Objectives

CASE specific objectives are:

- Act as a gathering point for researchers and students to encourage networking and collaboration.
- Present the latest scientific and technological advances to the academic community.
- Encourage advanced students to pursue research topics in the area of embedded systems.

Topics of Interest

Protocols and communications, RTOS, FPGAs and HDLs, Implementation of Embedded Systems, Embedded Linux, DSP, Embedded Software, ASICs, Processors architectures, and Robotics.

Paper and poster presentations

Original contributions for the categories “paper presentation”, “short paper”, and “poster presentation” will be received according to the guidelines published at the official website, where papers and posters will be published after the event.

Scholarship applications and lodging

Scholarships in the following categories will be awarded according to fund availability 1) Undergraduate students, 2) Graduate students, 3) University Teaching Staff and 4) Researchers.

Scholarship application forms will be published in the conference web page.

Important Dates

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| • Jan 16 to Apr 10: | Paper submission |
| • May 18 to Jun 1: | Rebuttal |
| • Jun 25: | Notification of acceptance |
| • Jun 26 to Jul 13: | Final paper submission |
| • August 15 to 17, 2012: | Conference |